

## *Methodologies for Hardware/Software Co-design*

**Instructor:** Dr. Margarida Jacome

**Course Description:** Techniques used to design complex hardware/software systems; emphasis on specification, modeling, estimation, partitioning, verification/validation, and synthesis.

**Syllabus:** The course will be covered at approximately the following pace:

Day 1: Hw/Sw Codesign of Embedded Systems – Introduction & overview

POLIS: A Framework for Codesign of Reactive Control Systems

Day 2 POLIS: A Framework for Codesign of Reactive Control Systems

Day 3: Modeling

Day 4: Modeling

Day 5: Midterm

Analysis and Estimation

Day 6: Analysis and Estimation

Day 7: Co-Simulation

Day 8: Do-Simulation

Day 9: System Level Partitioning, Synthesis/Implementation Generation

Day 10: Endterm

System Level Partitioning, Synthesis/Implementation Generation